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#### FDMS1D4N03S

# N-Channel PowerTrench<sup>®</sup> SyncFET<sup>TM</sup> 30 V, 211 A, 1.09 m $\Omega$

#### **Features**

- Max  $r_{DS(on)}$  = 1.09 m $\Omega$  at  $V_{GS}$  = 10 V,  $I_D$  = 38 A
- Max  $r_{DS(on)}$  = 1.3 m $\Omega$  at  $V_{GS}$  = 4.5 V,  $I_D$  = 35 A
- High Performance Technology for Extremely Low r<sub>DS(on)</sub>
- SyncFET<sup>TM</sup> Schottky Body Diode
- 100% UIL Tested
- RoHS Compliant

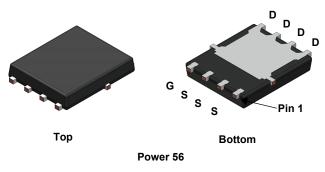
#### **General Description**

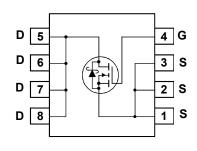
The FDMS1D4N03S has been designed to minimize losses in power conversion application. Advancements in both silicon and package technologies have been combined to offer the lowest  $r_{\text{DS}(\text{on})}$  while maintaining excellent switching performance. This device has the added benefit of an efficient monolithic schottky body diode.

#### **Applications**

- Synchronous Rectifier for DC/DC Converters
- Notebook Vcore/ GPU Low Side Switch
- Networking Point of Load Low Side Switch
- Telecom Secondary Sde Rectification







#### **MOSFET Maximum Ratings** T<sub>A</sub> = 25 °C unless otherwise noted.

Symbol	Param	eter		Ratings	Units
V <sub>DS</sub>	Drain to Source Voltage			30	V
V <sub>GS</sub>	Gate to Source Voltage			±16	V
	Drain Current -Continuous	T <sub>C</sub> = 25 °C	(Note 5)	211	
	-Continuous	T <sub>C</sub> = 100 °C	(Note 5)	134	_
ID	-Continuous	T <sub>A</sub> = 25 °C	(Note 1a)	38	Α
	-Pulsed		(Note 4)	1140	
E <sub>AS</sub>	Single Pulse Avalanche Energy		(Note 3)	384	mJ
$P_{D}$	Power Dissipation	T <sub>C</sub> = 25 °C		74	W
	Power Dissipation	T <sub>A</sub> = 25 °C	(Note 1a)	2.5	VV
T <sub>.J</sub> , T <sub>STG</sub>	Operating and Storage Junction Tempera	ature Range		-55 to +150	°C

#### **Thermal Characteristics**

$R_{\theta JC}$	Thermal Resistance, Junction to Case		1.7	°C/W
$R_{\theta,IA}$	Thermal Resistance, Junction to Ambient	(Note 1a)	50	C/VV

#### **Package Marking and Ordering Information**

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDMS1D4N03S	FDMS1D4N03S	Power 56	13 "	12 mm	3000 units

### **Electrical Characteristics** $T_J$ = 25 °C unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Off Chara	cteristics					
$BV_{DSS}$	Drain to Source Breakdown Voltage	I <sub>D</sub> = 1 mA, V <sub>GS</sub> = 0 V	30			V
ABV <sub>DSS</sub> ΔΤ <sub>J</sub>	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 10 mA, referenced to 25 °C		20		mV/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 24 V, V <sub>GS</sub> = 0 V			500	μΑ
$I_{GSS}$	Gate to Source Leakage Current	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$			±100	nA

#### On Characteristics

V <sub>GS(th)</sub>	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}$ , $I_D = 1 \text{ mA}$	1	1.6	3	V
$\Delta V_{GS(th)} \Delta T_J$	Gate to Source Threshold Voltage Temperature Coefficient	I <sub>D</sub> = 10 mA, referenced to 25 °C		-4		mV/°C
r <sub>DS(on)</sub>	Static Drain to Source On Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 38 A		8.0	1.09	
		$V_{GS} = 4.5 \text{ V}, I_D = 35 \text{ A}$		1.0	1.3	mΩ
		$V_{GS} = 10 \text{ V}, I_D = 38 \text{ A}, T_J = 125 ^{\circ}\text{C}$		1.2	1.7	
9 <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> = 5 V, I <sub>D</sub> = 38 A		281		S

#### **Dynamic Characteristics**

C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz		7320	10250	pF
C <sub>oss</sub>	Output Capacitance			1950	2730	pF
C <sub>rss</sub>	Reverse Transfer Capacitance			101	180	pF
$R_g$	Gate Resistance		0.1	0.5	1.5	Ω

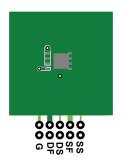
#### **Switching Characteristics**

t <sub>d(on)</sub>	Turn-On Delay Time		21	33	ns
t <sub>r</sub>	Rise Time	V <sub>DD</sub> = 15 V, I <sub>D</sub> = 38 A,	6	12	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	$V_{GS}$ = 10 V, $R_{GEN}$ = 6 $\Omega$	51	82	ns
t <sub>f</sub>	Fall Time		5	10	ns
Qg	Total Gate Charge	V <sub>GS</sub> = 0 V to 10 V	102	143	nC
Qg	Total Gate Charge	$V_{GS} = 0 \text{ V to } 4.5 \text{ V}$ $V_{DD} = 15 \text{ V}$	46	65	nC
Q <sub>gs</sub>	Gate to Source Charge	I <sub>D</sub> = 38 A	18		nC
Q <sub>gd</sub>	Gate to Drain "Miller" Charge		9		nC

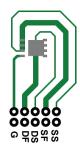
#### **Drain-Source Diode Characteristics**

V <sub>SD</sub> Source to Drain Diode Forwa	Course to Drain Diado, Fanyard Valtage	$V_{GS} = 0 \text{ V}, I_S = 2.1 \text{ A}$ (Note 2)	0.7	1.2	V
	Source to Drain Diode Forward Voltage	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 38 A (Note 2)	8.0	1.3	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> = 38 A, di/dt = 246 A/μs	44	70	ns
Q <sub>rr</sub>	Reverse Recovery Charge	1F - 36 A, αι/αι - 246 A/μs	70	112	nC

<sup>1.</sup> R<sub>BJA</sub> is determined with the device mounted on a 1 in<sup>2</sup> pad 2 oz copper pad on a 1.5 x 1.5 in. board of FR-4 material. R<sub>BJC</sub> is guaranteed by design while R<sub>BCA</sub> is determined by the user's board design.



a) 50 °C/W when mounted on a 1 in<sup>2</sup> pad of 2 oz copper



b) 125 °C/W when mounted on a minimum pad of 2 oz copper.

<sup>2.</sup> Pulse Test: Pulse Width < 300  $\mu$ s, Duty cycle < 2.0%. 3. E<sub>AS</sub> of 384 mJ is based on starting T<sub>J</sub> = 25 °C, L = 3 mH, I<sub>AS</sub> =16 A, V<sub>DD</sub> =30 V, V<sub>GS</sub> = 10 V. 100% tested at L = 0.1 mH, I<sub>AS</sub> = 52 A. 4. Pulse Id please refer to Fig.11 SOA curve for detail.

<sup>5.</sup> Computed continuous current limited to Max Junction Temperature only, actual continuous current will be limited by thermal & electro-mechanical application board design

#### Typical Characteristics T<sub>J</sub> = 25 °C unless otherwise noted.

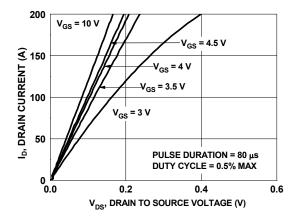


Figure 1. On Region Characteristics

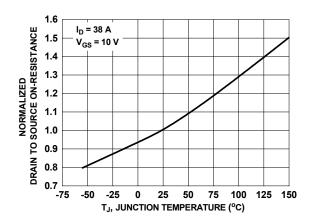


Figure 3. Normalized On Resistance vs. Junction Temperature

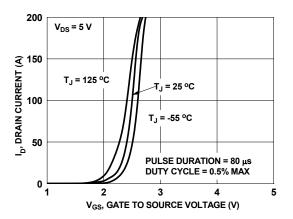


Figure 5. Transfer Characteristics

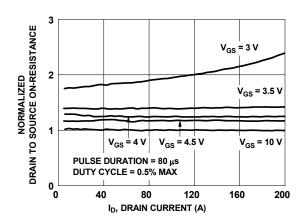


Figure 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

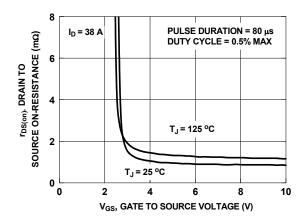


Figure 4. On-Resistance vs. Gate to Source Voltage

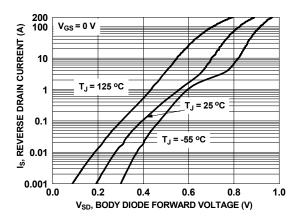


Figure 6. Source to Drain Diode Forward Voltage vs. Source Current

### **Typical Characteristics** $T_J = 25$ °C unless otherwise noted.

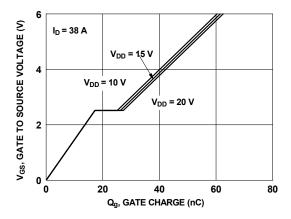


Figure 7. Gate Charge Characteristics

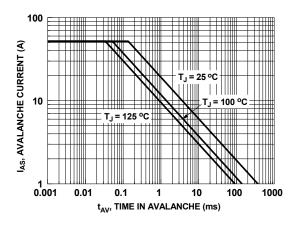


Figure 9. Unclamped Inductive Switching Capability

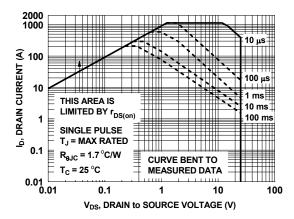


Figure 11. Forward Bias Safe Operating Area

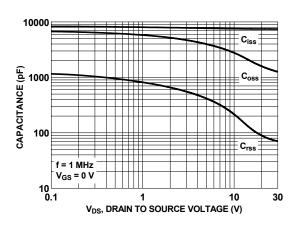


Figure 8. Capacitance vs. Drain to Source Voltage

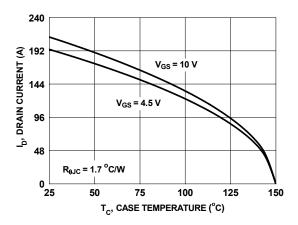


Figure 10. Maximum Continuous Drain Current vs. Case Temperature

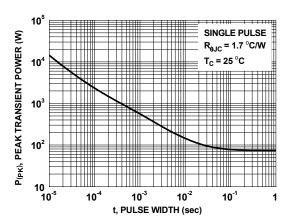


Figure 12. Single Pulse Maximum Power Dissipation

### Typical Characteristics $T_J$ = 25 °C unless otherwise noted.

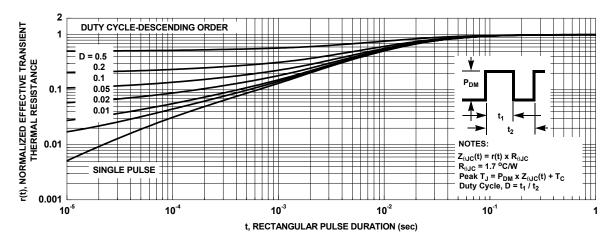


Figure 13. Junction-to-Case Transient Thermal Response Curve

#### Typical Characteristics (continued)

# SyncFET<sup>TM</sup> Schottky body diode Characteristics

Fairchild's SyncFET<sup>TM</sup> process embeds a Schottky diode in parallel with PowerTrench MOSFET. This diode exhibits similar characteristics to a discrete external Schottky diode in parallel with a MOSFET. Figure 14 shows the reverse recovery characteristic of the FDMS1D4N03S.

Schottky barrier diodes exhibit significant leakage at high temperature and high reverse voltage. This will increase the power in the device.

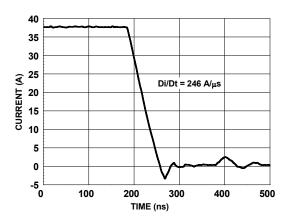


Figure 14. FDMS1D4N03S SyncFET<sup>TM</sup> Body Diode Reverse Recovery Characteristic

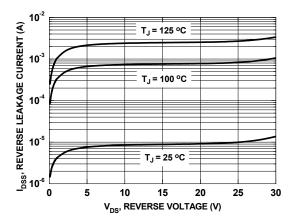


Figure 15. SyncFET<sup>TM</sup> Body Diode Reverse Leakage vs. Drain-Source Voltage



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