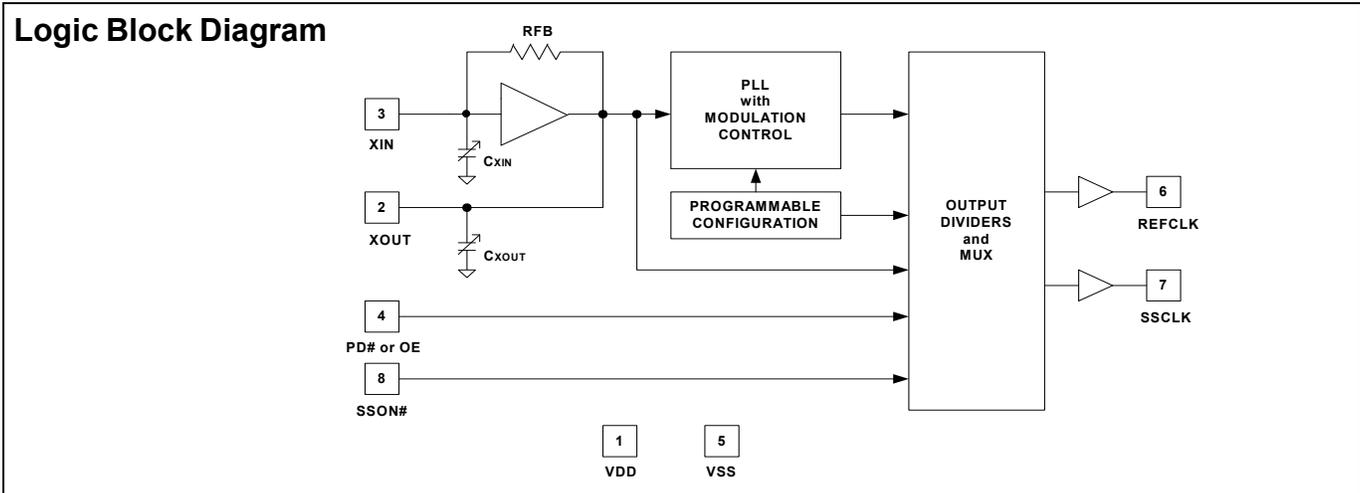# Programmable Spread Spectrum Clock Generator for EMI Reduction

Description

This document describes the programmed values and functions of CY25103. Refer to the CY25100 data sheet for detailed product description and specifications.

Logic Block Diagram

Pin Configuration

Figure 1. CY25103-2 8-pin SOIC/Tssop
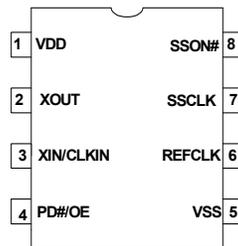
Table 1. Programmed Values and Pin Functions

Pin	Parameter	Value	Unit
3	XIN	12.00	MHz
7	SSCLK	48	MHz
7	Spread Percent	± 0.25	%
2 and 3	C_{XIN} and C_{XOUT}	36	pF
6	REFCLK	Off	MHz
4	PD#/OE	OE	n/a
7	Modulation frequency	31.5	kHz

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Table 2. CY25103–2 - Spread Percentage

Part Number	Spread Percentage	Parallel Crystal Load
CY25103–2	±0.25, Center Spread	18 pF

Ordering Information

Part Number	Package Type	Product Flow
CY25103SXC–2	8-pin, SOIC	Commercial, 0° to 70°C
CY25103SXC–2T	8-pin, SOIC, Tape and Reel	Commercial, 0° to 70°C

Document History Page

Document Title: CY25103 Programmable Spread Spectrum Clock Generator for EMI Reduction **Document Number: 38-07582**				
Rev.	**ECN No.**	**Orig. of Change**	**Submission Date**	**Description of Change**
**	129464	RGL	09/09/03	New Data Sheet
*A	2628846	CXQ/PYRS	12/31/08	Removed "ADVANCE INFORMATON" Removed references to CY25103-1 and CY25103-3 Updated "Ordering Information" table

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